

## INVENTOR INFORMATION

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## CORRESPONDENCE INFORMATION

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## APPLICATION INFORMATION

Title Line One:: PLANAR DIELECTRIC INTEGRATED CIRCUIT  
Total Drawing Sheets:: 8  
Formal Drawings?:: Yes  
Application Type:: Utility  
Docket Number:: P/1071-1520  
Secrecy Order in Parent Appl.?:: No

#### CONTINUITY INFORMATION

This application is a:: DIVISION OF  
> Application One:: 09/031,981  
Filing Date:: 02-26-1998

#### PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 9-44162  
Filing Date:: 02-27-1997  
Country:: Japan  
Priority Claimed:: Yes

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